

FIG. 1

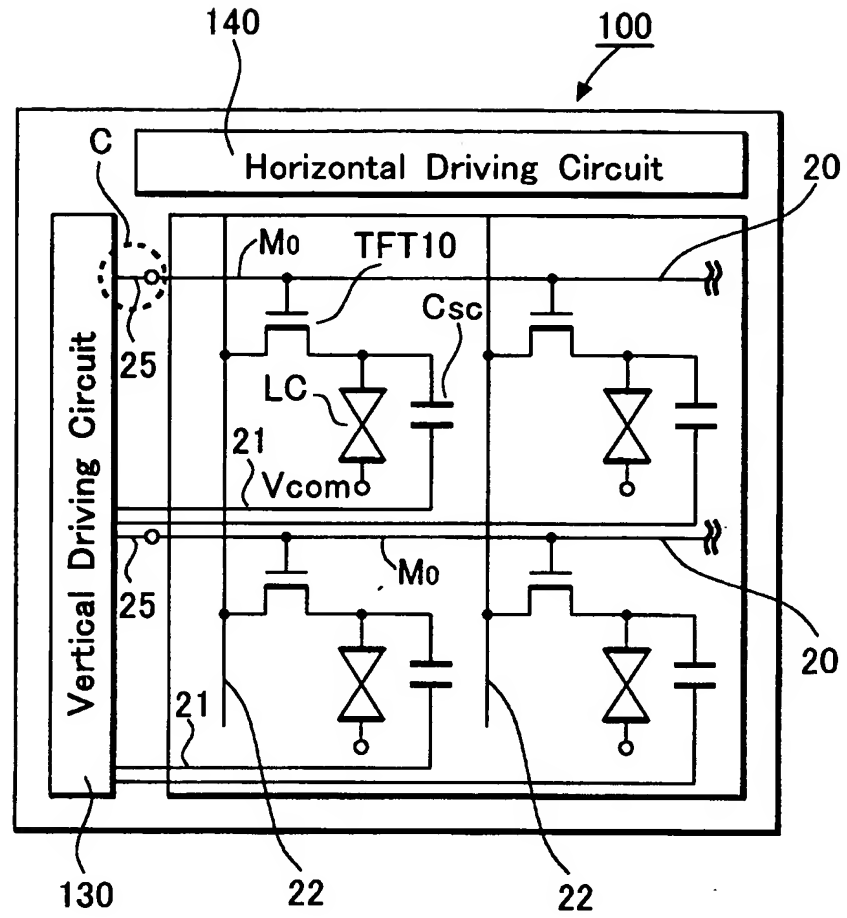


FIG. 2A

Output Portion of the Vertical Driving Circuit 130

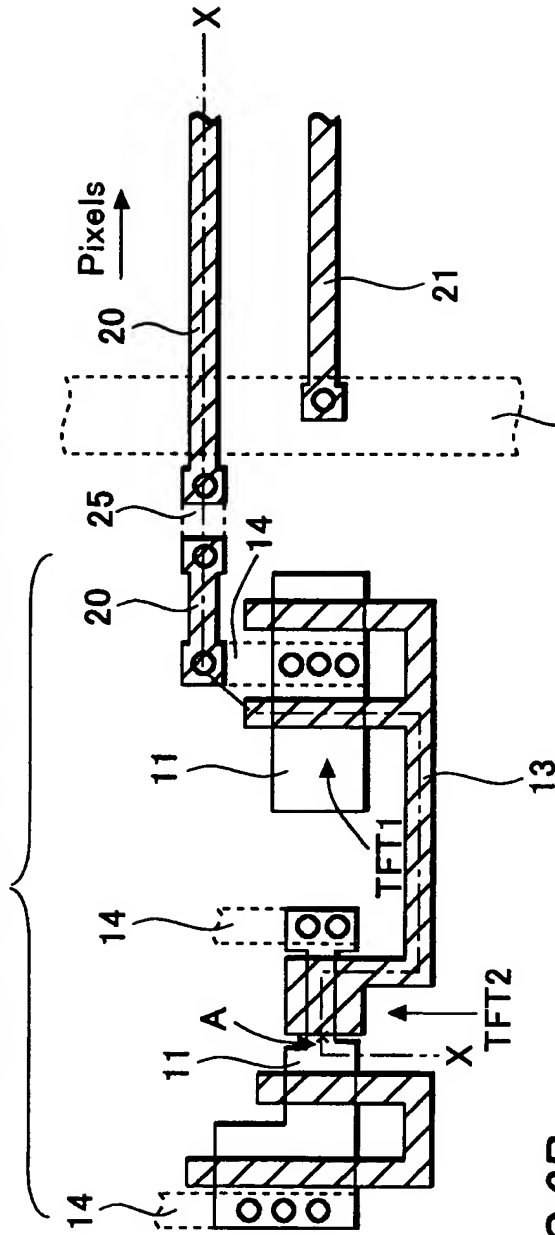


FIG. 2B

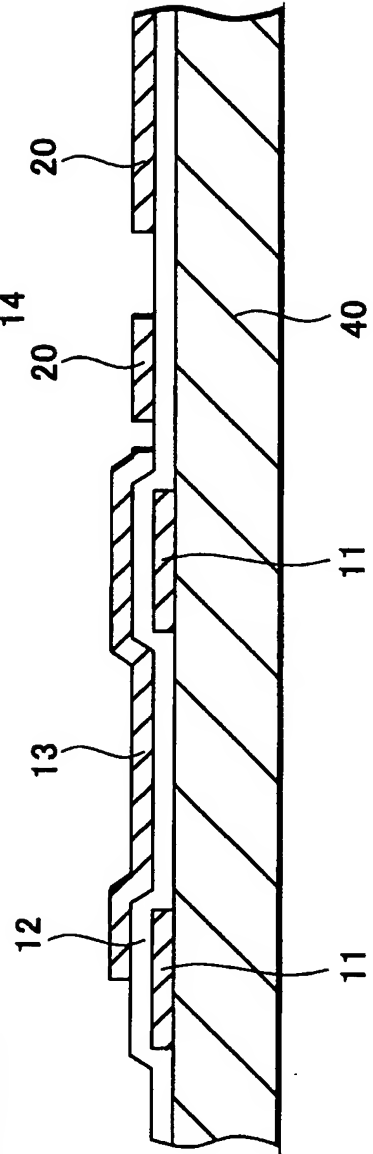


FIG. 3

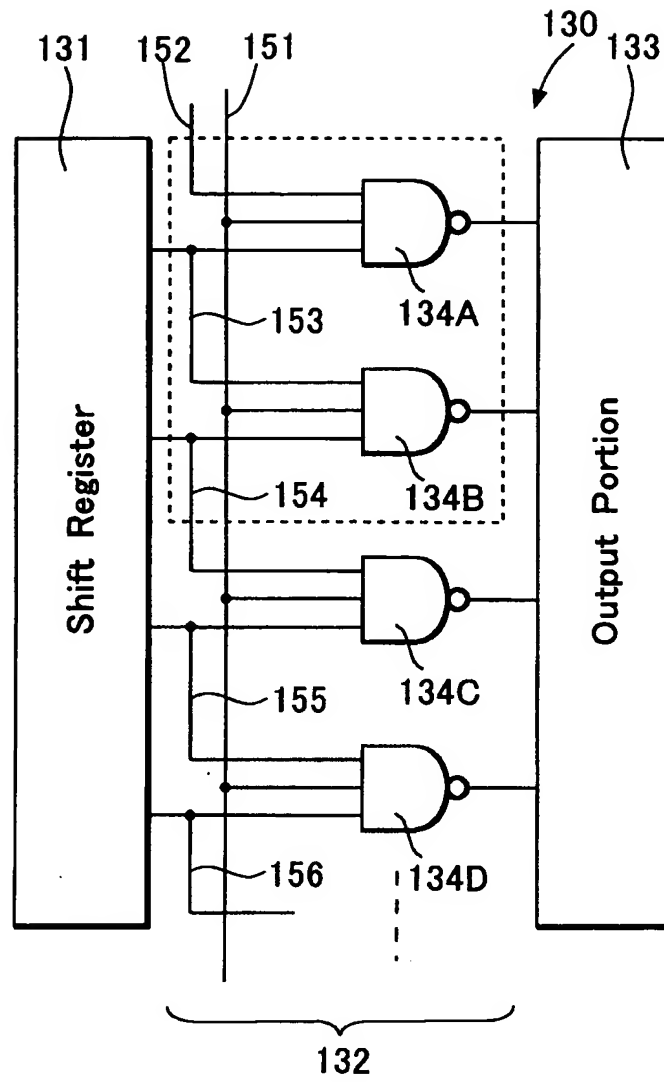


FIG. 4

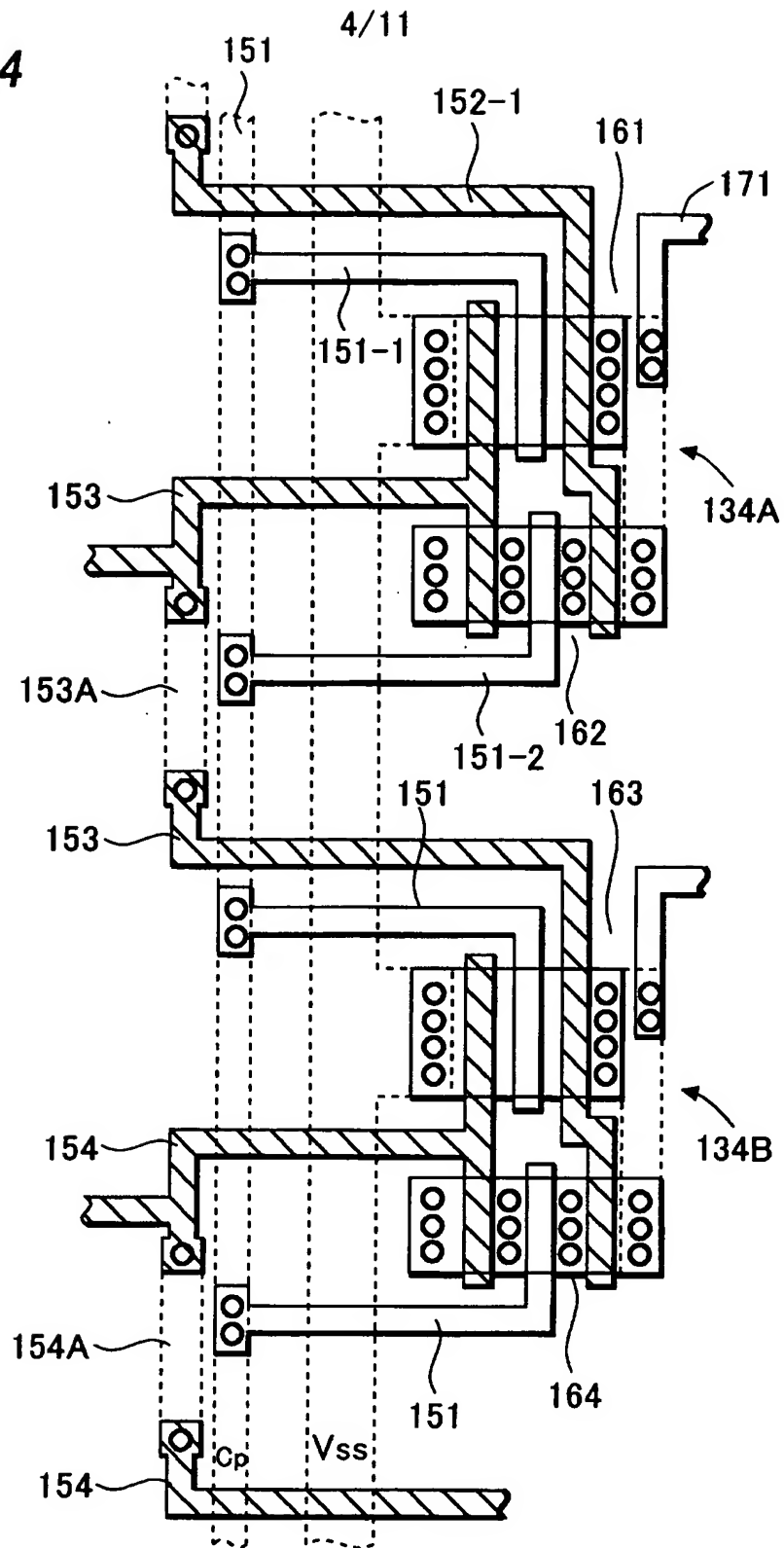


FIG.5A

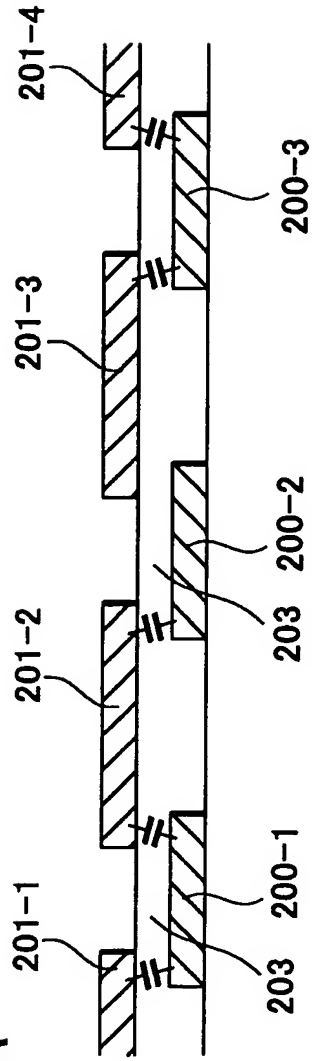


FIG.5B

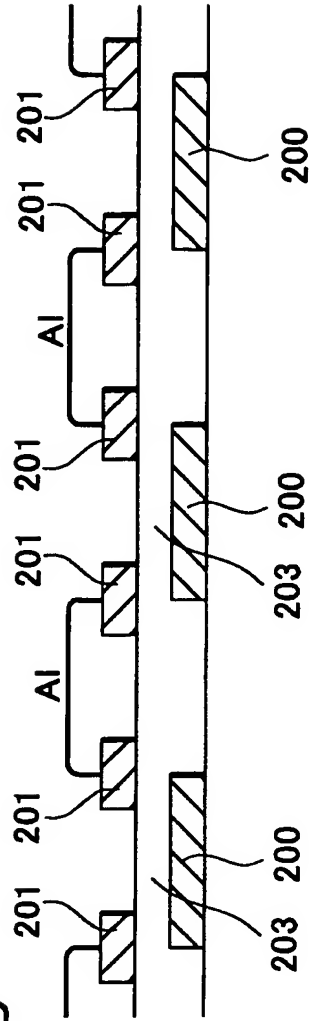


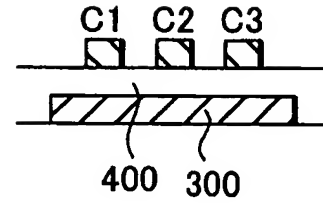
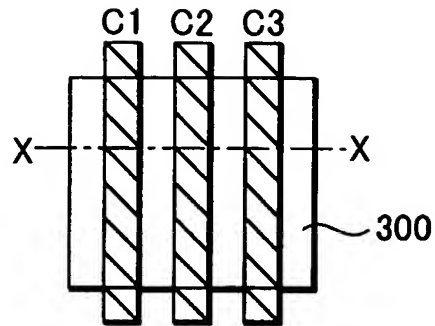
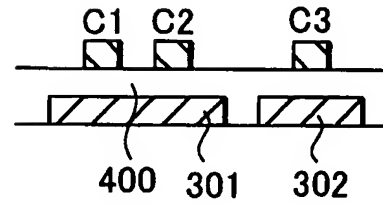
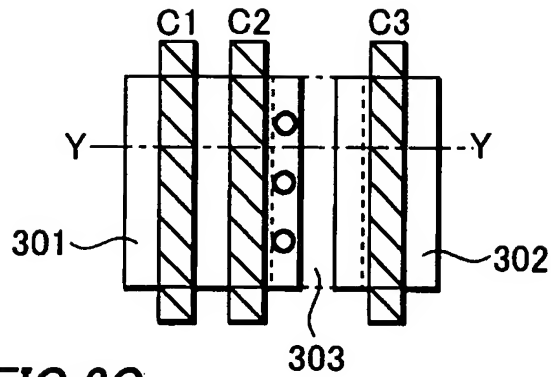
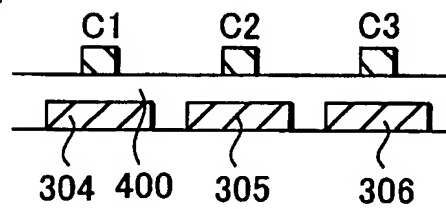
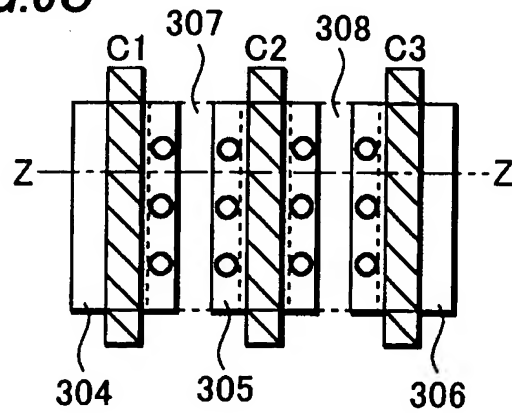
FIG.6A**FIG.6B****FIG.6C**

FIG.7A Output Portion of the Vertical Driving Circuit 130

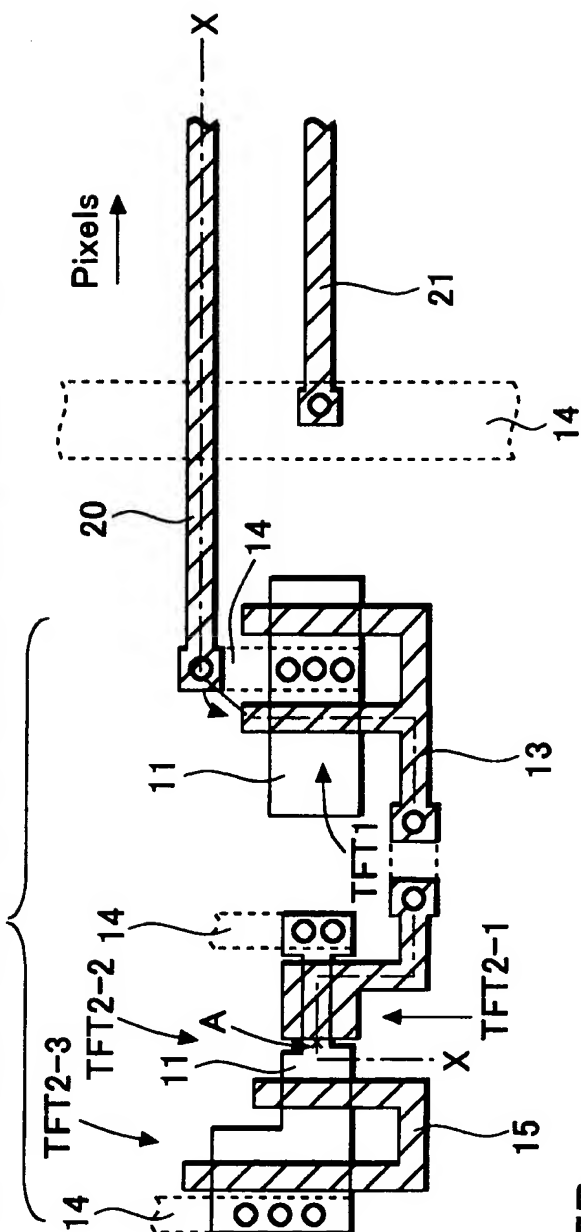


FIG.7B

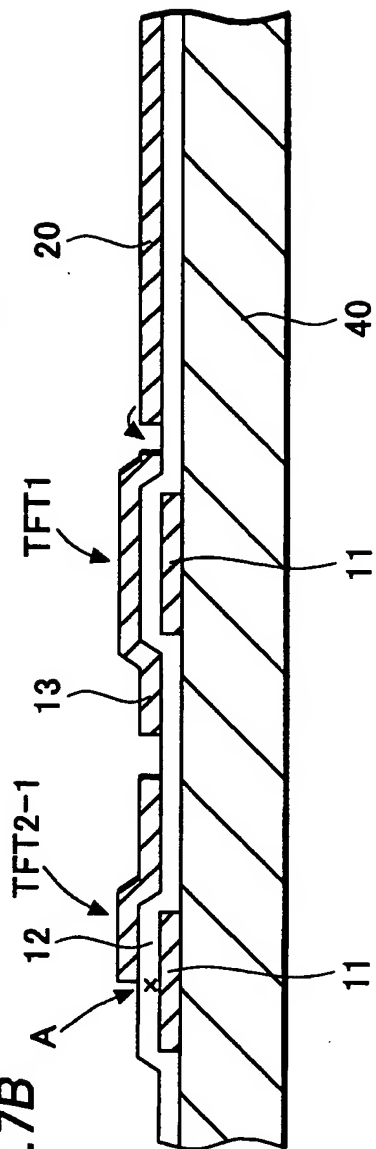


FIG.8

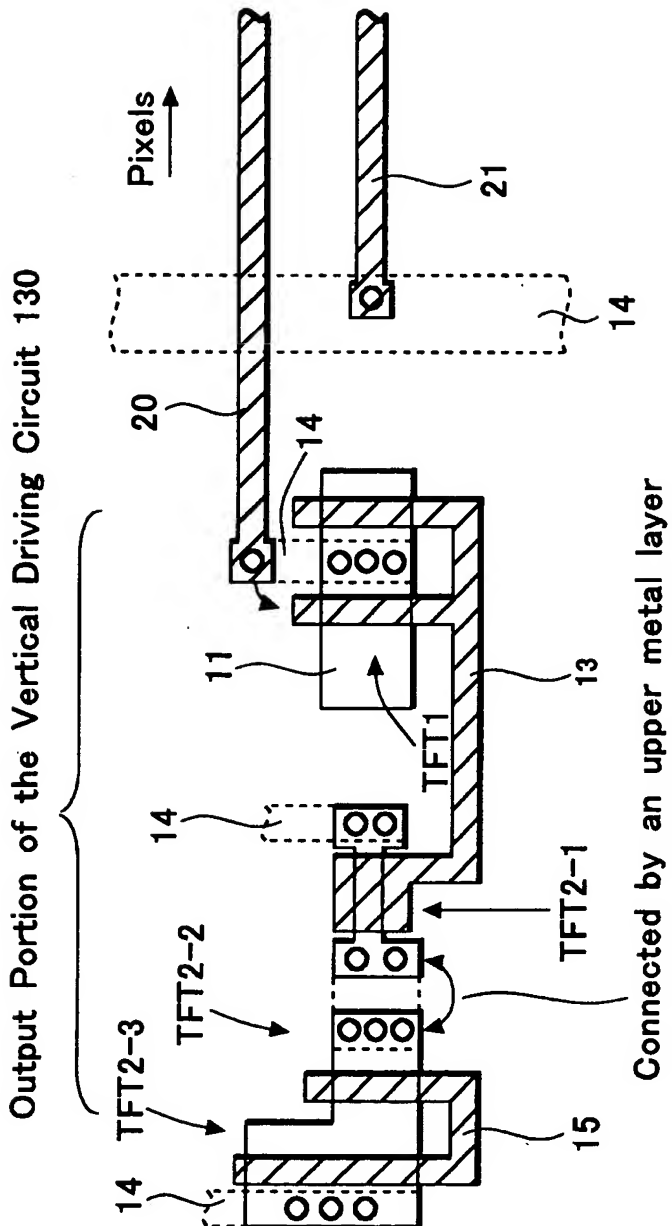


FIG. 9

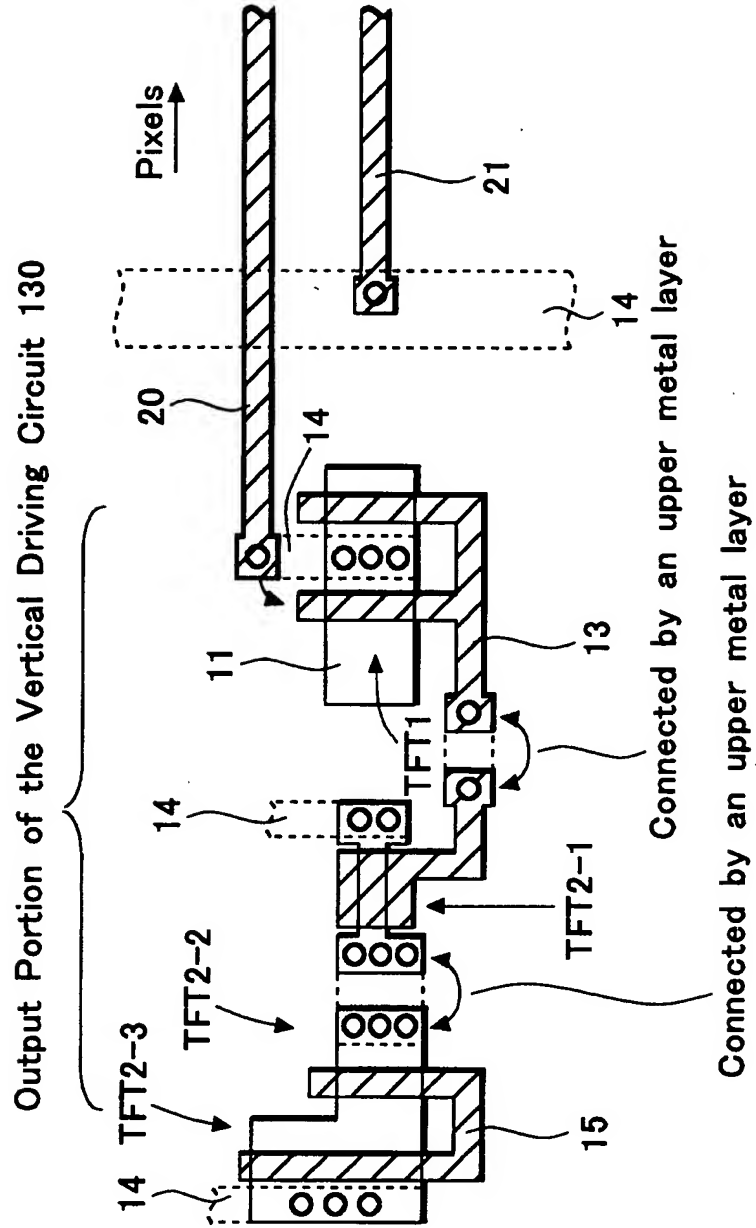


FIG. 10

Prior Art

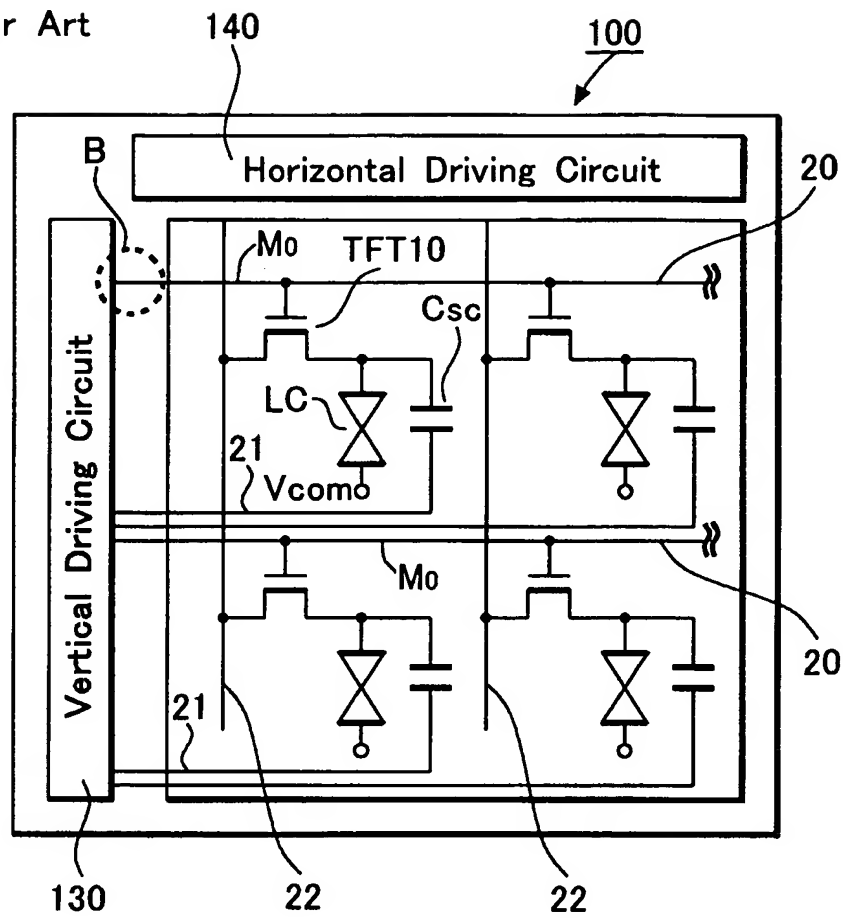


FIG. 11A
Prior Art

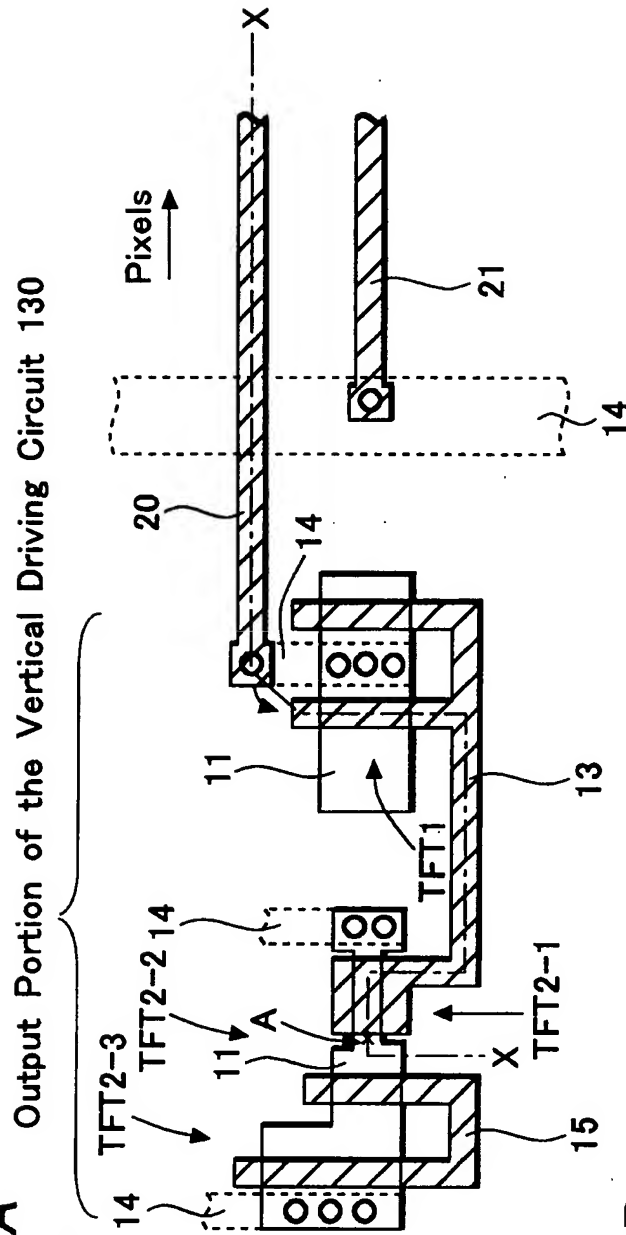


FIG. 11B
Prior Art

